

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) ~~An apparatus of power controller~~
~~controlling in a transmitter for receiving an emission~~
~~signals signals and outputting to an antenna, comprising:~~

~~a plurality of stage amplifiers for receiving the emission~~
~~signal signals and amplifying the power thereof;~~

~~a plurality of matching circuits connected between the stage~~
~~amplifiers for matching with the stage amplifiers, respectively;~~

Al
~~at least one power detector for detecting the power of the at~~
~~least one stage amplifiers amplifier and generating detection~~
~~signals signals, respectively; and~~

~~a bias control circuit for receiving the detection signals of~~
~~the at least one power detector, thereby generating a bias of each~~
~~of the stage amplifiers in order to optimize the efficiency of~~
~~each of the stage amplifiers according to the magnitude of the~~
~~power of each of the stage amplifiers.~~

2. (Currently Amended) ~~The An apparatus for power controlling~~
~~in a transmitter for receiving an emission signal and outputting~~
~~to an antenna, comprising: power controller according to claim 1,~~

~~a plurality of stage amplifiers for receiving the emission~~
~~signal and amplifying the power thereof;~~

a plurality of matching circuits connected between the stage amplifiers for matching with the stage amplifiers, respectively;
at least one power detector for detecting the power of the at least one stage amplifier and generating detection signal; and
a bias control circuit for receiving the detection signal of the at least one power detector, thereby generating a bias of each of the stage amplifiers in order to optimize the efficiency of each of the stage amplifiers according to the magnitude of the power of each of the stage amplifiers;

wherein the bias control circuit comprises a comparator, thereby outputting a bypass control signal when the power controller is disabled.

3. (Currently Amended) The apparatus power controller according to claim 2, further comprising a signal bypass unit, thereby grounding the emission signal signals for preventing the emission signal signals from being transmitted to each of the stage amplifiers when the bypass control signal is enabled.

4. (New) An apparatus of power controlling in a transmitter for receiving an emission signal and outputting to an antenna, comprising:

a plurality of stage amplifiers for receiving the emission signal and amplifying the power thereof;

a plurality of matching circuits connected between the stage amplifiers for matching with the stage amplifiers, respectively;

a plurality of power detectors for detecting the power of the amplifiers and generating detection signals; and

a bias control circuit for receiving the detection signals of the power detectors, thereby generating a bias of each of the stage amplifiers in order to optimize the efficiency of each of the stage amplifiers according to the magnitude of the power of each of the stage amplifiers.

5. (New) The apparatus according to claim 4, wherein the bias control circuit comprises a comparator, thereby outputting a bypass control signal when the power controller is disabled.

6. (New) The apparatus according to claim 5, further comprising a signal bypass unit, thereby grounding the emission signal for preventing the emission signal from being transmitted to each of the stage amplifiers when the bypass control signal is enabled.